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Inventor: **Pai-Hung Pan**
Title: **Semiconductor Processing Methods of Forming a Conductive Gate and Line**
Assignee: **Micron Technology, Inc.**

INFORMATION DISCLOSURE STATEMENT

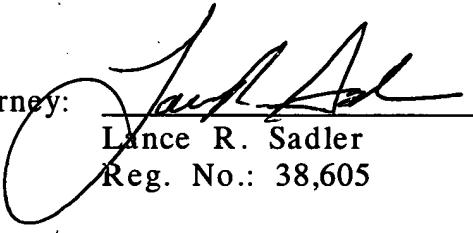
References -- See Attached Form PTO-1449

The attached form PTO-1449 is submitted in compliance
with 37 CFR §1.56.

Respectfully submitted,

Dated: 4/13/98

Attorney:


Lance R. Sadler
Reg. No.: 38,605